

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****Patent**

In re Application: Bryant et al.

Today's Date: July 15, 2002

Serial No.: 09/886,823

Group Art Unit: 2826

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Examiner: Abraham, F.

Fax: (703) 746-4125

**FAX RECEIVED****Title: Double Gated Transistor and Method of Fabrication**

FEB 6 2003

**AMENDMENT UNDER 35 USC 132**

TECHNOLOGY CENTER 2800

To the Assistant Commissioner of Patents  
Washington, D.C. 20231

Dear Sir:

In response to the Office Action of February 28, 2002, in which claims 26 and 27 stand objected to, claims 25 and 28-35 stand rejected, and claims 36-40 stand allowed, kindly reconsider such rejections and objections in light of the comments below. Applicants do not believe that any fee is due for the submission and consideration of the present paper; however, should the Patent Office determine that such a fee is due, Applicants hereby authorize the Patent Office to charge their Deposit Account 09-0456 such sums as may be due for the submission and consideration of this paper.

**CERTIFICATE OF MAILING**

I hereby certify that, on the date shown below, this correspondence is being:

**MAIL**

☐ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.

Date: 7/15/02**FACSIMILE**

☒ transmitted by facsimile to the Patent and Trademark Office.

PAT BLAIR

Name

Pat Blair

Signature

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